

REPLACEMENT DRAWINGS

Replace the following Figures with the 6 enclosed drawing sheets: 1, 2, 3A-3F, 4 and 6.

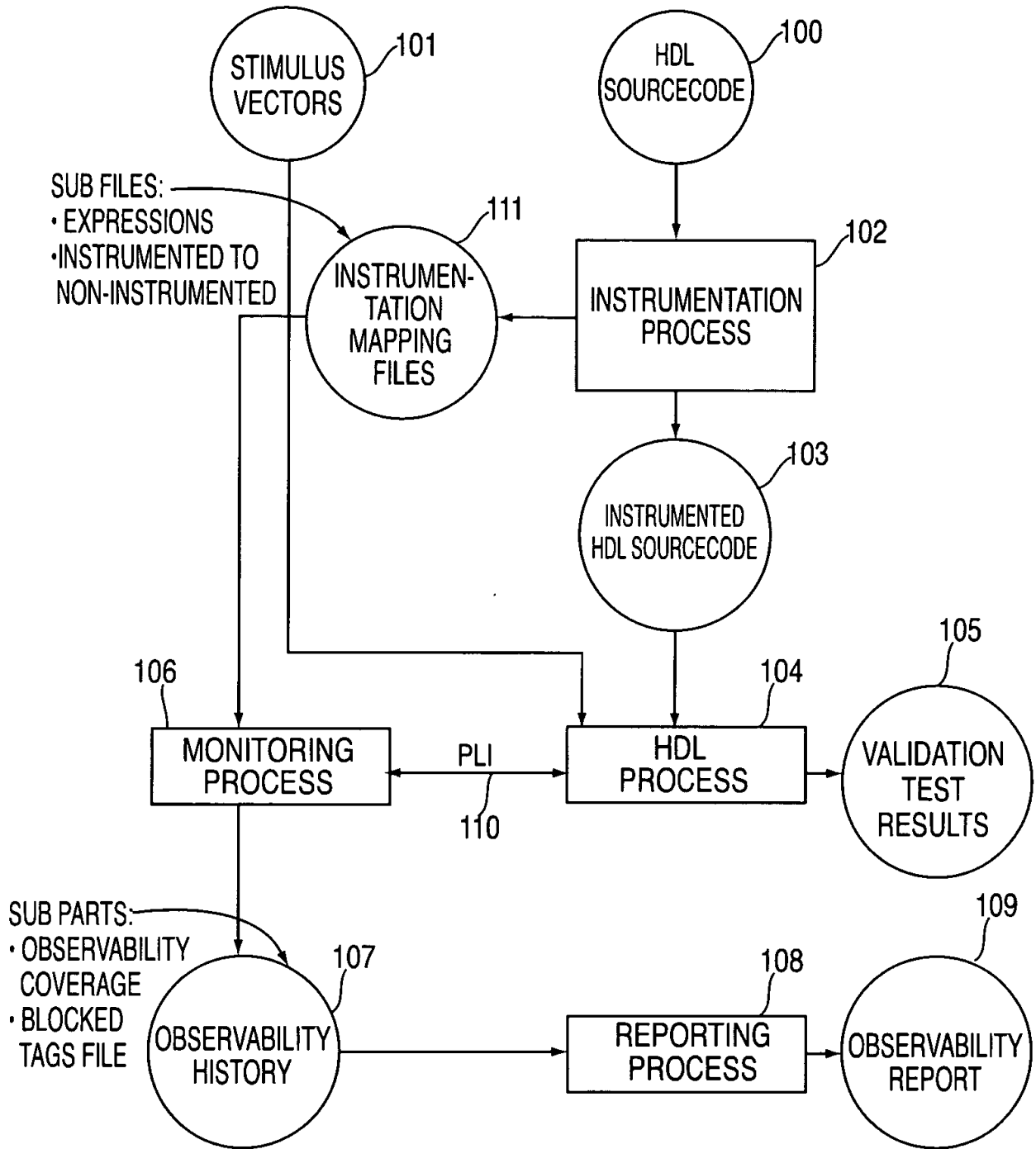


FIG. 1

FIG. 2
(TAG VALUE)

200

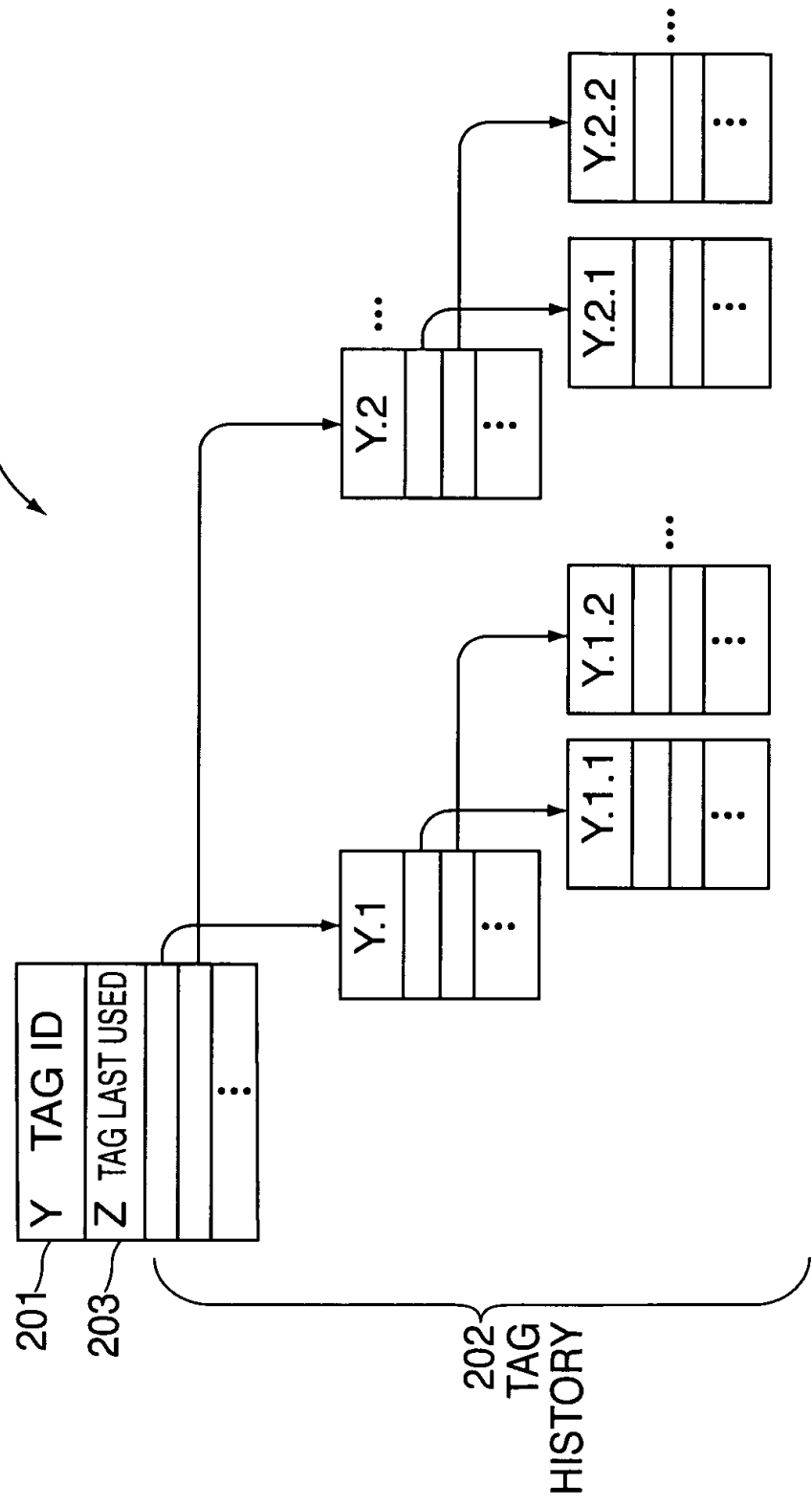


FIG. 3A
 (ABSTRACTED ASSIGNMENT STATEMENT)

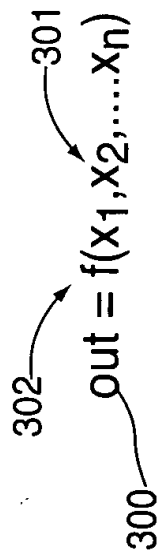


FIG. 3B
 (TAG VALUE FOR ABSTRACTED ASSIGNMENT STATEMENT)

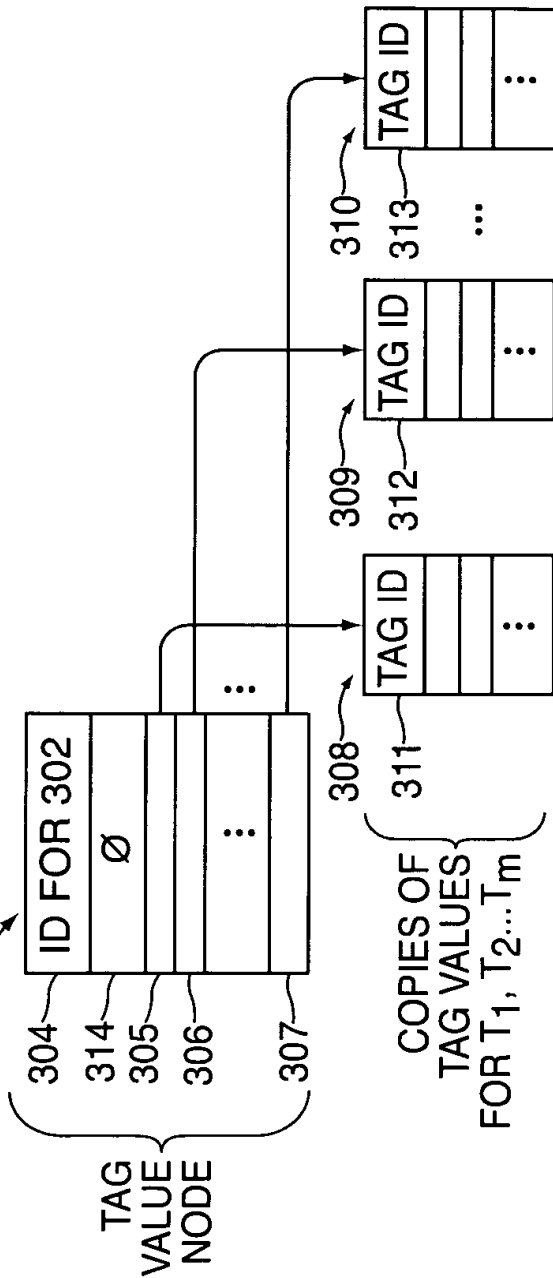


FIG. 3C
 (EXAMPLE VERILOG HDL ASSIGNMENT STATEMENT)

330 \rightarrow
 $out = a \& b;$

FIG. 3D

(EXAMPLE VERILOG HDL ASSIGNMENT STATEMENT)

$\text{out_1} = (\text{in_3} \& \text{in_2}) \parallel (\text{in_1} \& \text{in_0})$

FIG. 3E

(PARSE TREE OF RHS OF ASSIGNMENT STATEMENT
WITH RULE-BASED TAG PROPAGATION)

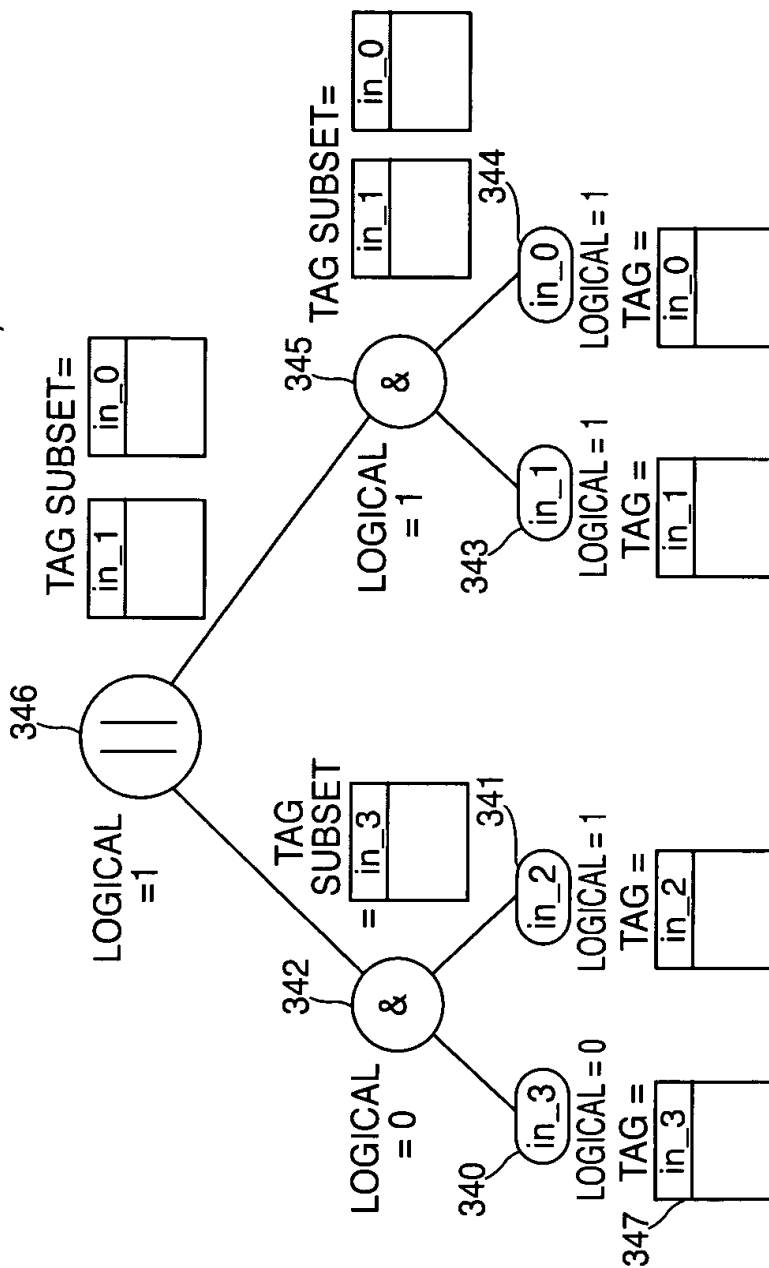


FIG. 3F
 (RESULTING TAG VALUE FROM RULE-BASED PROPAGATION)

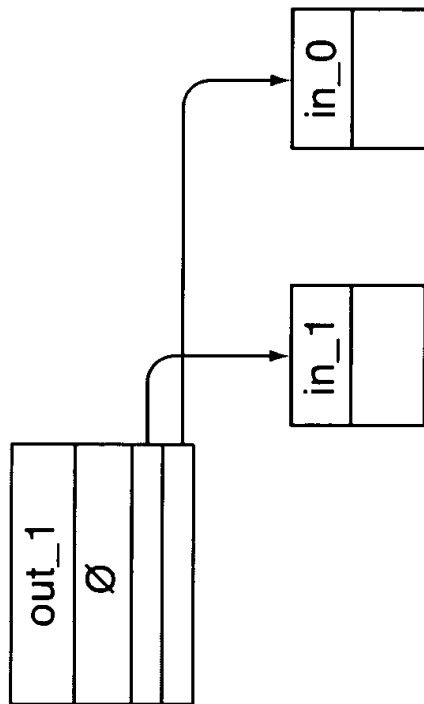


FIG. 4
 (ABSTRACTED CONDITIONALLY EXECUTED ASSIGNMENT)

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    340 → if (f1 (w1, w2, ..., wt) )
           341 → if (f2 (z1, z2, ..., zp) )
                     300 → out = f (x1, x2, ..., xn) ;
                           302
  
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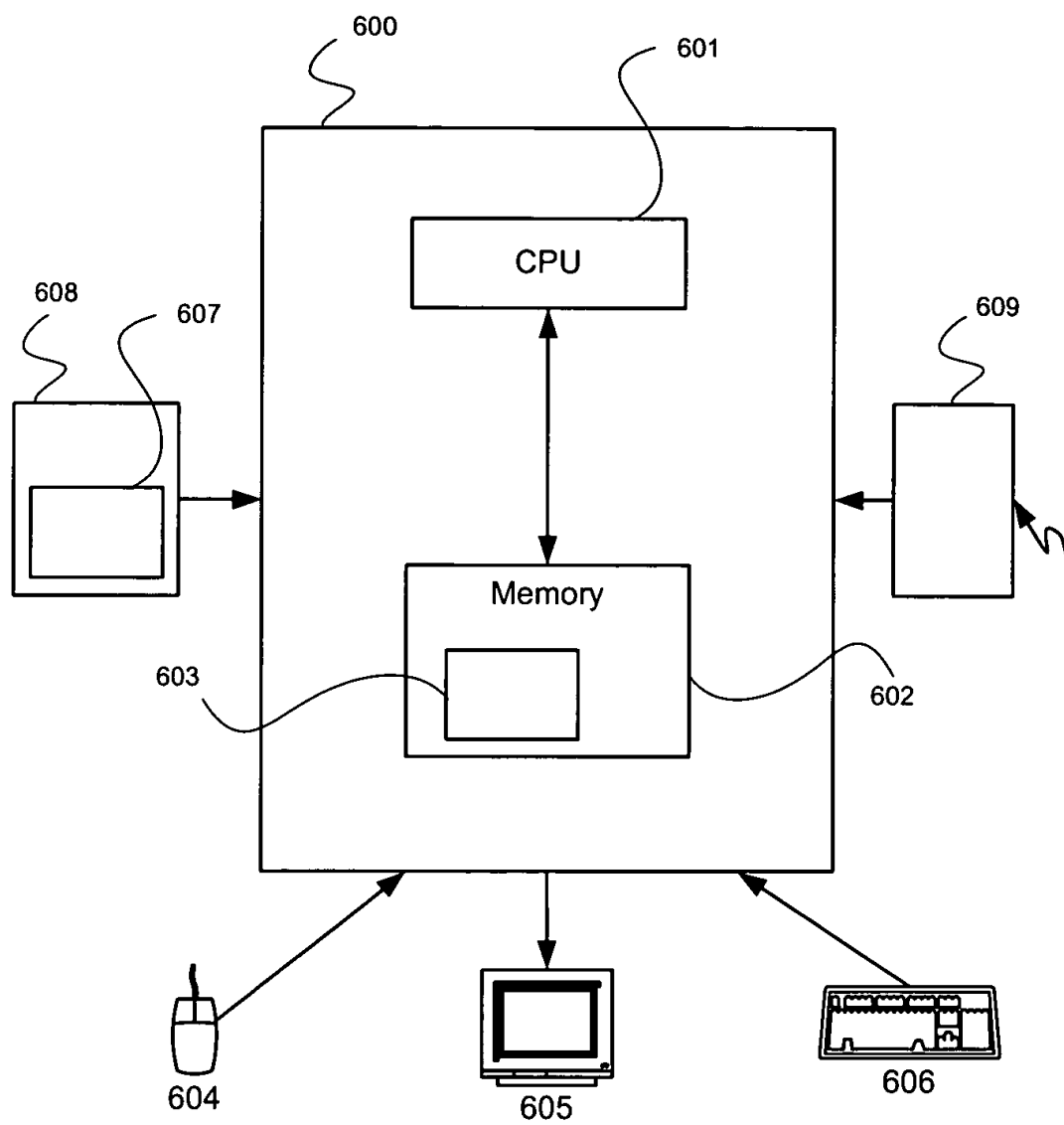


FIGURE 6